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cat*

prevent electrical connection between second electrode plate 32 and said through via-terminals 38. Instead, second electrode plate 32 is electrically connected to the alternative through-via terminals 36."

**IN THE CLAIMS:**

Kindly cancel claim 4 without prejudice.

Please amend claims 1, 5, 6 and 7 of the above-identified application to appear as below.

Please note that the respective amendments to claims 5, 6, and 7 are requested merely to modify typographical errors in such claims. The amendments regarding claims 5, 6, and 7 do not add any new matter and are in no way intended as modifications for purposes of patentability.

1. (Amended) A multi-layer ceramic capacitive device comprising:
  - a plurality of first electrode plates with a via centrally located therein, said via having a first diameter, wherein said first electrode plates have a circular opening having a second diameter therein and wherein said circular opening surrounds said via;
  - a plurality of second electrode plates with a via centrally located therein, said via having said first diameter, wherein said second electrode plates have a perimeter region of said plates removed and wherein said pluralities of first and second electrode plates are respectively interleaved in alternating planes to form a capacitive stack;
  - a first terminal surrounding the perimeter of the capacitive stack and in electrical connection with each of said plurality of first electrode plates; and
  - a second terminal in said via and in electrical connection with each of said plurality of second electrode plates.

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- 1.6. (Amended) A multi-layer ceramic capacitive array mounted to a circuit board

having a plurality of contacts in a plurality of rows, said plurality of contacts within each of said plurality of rows having an alternating pattern of polarity, said array comprising:

a plurality of multi-layer ceramic capacitive devices wherein each of said devices have a first terminal surrounding the perimeter of said capacitive device and in electrical connection with each of a plurality of first electrode plates and a second terminal centrally located in said capacitive device in a through-via and in electrical connection with each of a plurality of second electrode plates, said first and second electrode plates being interleaved to form said capacitive device;

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cont*

wherein said plurality of capacitive devices are mounted to said circuit board in a diamond arrangement such that said first terminal is in electrical connection with four of said plurality of contacts, one each at the corners of the device and wherein said second terminal is in electrical connection with one of said plurality of contacts of an opposing polarity.

**8.6.** (Amended) A multi-layer ceramic capacitive device mounted to a circuit board having a plurality of contacts in a plurality of rows, said plurality of contacts within each of said plurality of rows having an alternating pattern of polarity, said device comprising:

a plurality of first electrode plates with a plurality of vias located therein;  
a plurality of second electrode plates with a plurality of vias located therein, said pluralities of first and second electrode plates interleaved to form said capacitive device wherein said plurality of vias in said first electrode plates, said plurality of vias in said second electrode plates and said plurality of contacts on said circuit board are aligned;

a plurality of first terminals corresponding to alternating ones of said plurality of contacts in each of said plurality of rows such that all of said first terminals have the same polarity; and

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*(int)*

a plurality of second terminals corresponding to alternating ones of said plurality of contacts in each of said plurality of rows such that all of said second terminals have the same polarity which opposes the polarity of said first terminals.

*97.* (Amended) A multi-layer ceramic capacitive device mounted to a circuit board having a plurality of contacts in a plurality of rows, said plurality of contacts within each of said plurality of rows having an alternating pattern of polarity as in claim *8*, wherein said device is generally rectangular.

*Please also add the following claims, as hereafter presented:*

*6.8.* (New) A multi-layer ceramic capacitive device as in claim 1, further comprising a portion of solder generally filling said via for providing electrical connection to each of said plurality of second electrode plates.

*4.9.* (New) A multi-layer ceramic capacitive device as in claim 2, wherein said device is mounted to a substrate such that electrical connection to said first terminal is provided at selected corners of said generally square-shaped device.

*5.10.* (New) A multi-layer ceramic capacitive device as in claim 2, wherein said device is mounted to a substrate such that electrical connection to said first terminal is provided at each of the four corners of said generally square-shaped device.

*10.11.* (New) A multi-layer ceramic capacitive device as in claim *6*, wherein each of said first terminals is connected to a selected location within each first electrode plate and wherein each of said second terminals is connected to a selected location within each second electrode plate.